

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:



Warren M. Farnworth  
Alan G. Wood  
William M. Hiatt  
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David R. Hembree  
Kyle K. Kirby  
Pete A. Benson

Serial No.: 10/730,333

ART UNIT: 2822

Filing Date: 12/08/2003

EXAMINER: Potter, Roy

For: WAFER LEVEL METHODS FOR FABRICATING  
MULTI-DICE CHIP SCALE SEMICONDUCTOR  
COMPONENTS (AS AMENDED)

Attorney Docket No. 02-1500.1

AMENDMENT

October 7, 2005

Mail Stop Amendment  
Commissioner of Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

This Amendment is in response to the Office Action dated 09/30/2005, having a statutory period for response set to expire on 12/30/2005. Please amend the application as follows.